Vacancy: DSP/ FPGA Engineer

Celestia Satellite Test & Simulation BV (C-STS) provides innovative high-tech solutions to the international space community to support spacecraft development, AIT and post launch services.
The company operates from Noordwijk, The Netherlands.

Our core activities include the design/development and support of high-complexity electronics based systems for Simulation & Test (EGSE) Products, TTC & TM/TC Systems, High-Rate & TTC Modems and Data Processing Systems.

Due to company's current growth we are searching for a **DSP/FPGA Engineer**. The candidate will be responsible for the full development, including requirement analysis, FPGA architectural/detailed design, implementation and validation of digital electronics within high-end modem products.

The main tasks of the candidate will be:

- Requirements and architecture analysis, interface definition, and (functional) decomposition and mapping the architecture down to component/FPGA level
- VHDL design, implementation, simulation and verification of modulation, demodulation cores utilising the state of the art DSP techniques
- Interaction with other electronic design engineers, VHDL and FPGA specialists, modem specialists, software engineers, electronic design engineers, project management
- Support the product development team during the product qualification activities
- Document the requirement analysis, detailed design; simulation results, validation/verification procedures, test reports and interface control documents

Required key qualifications, skills and experience for the candidate:

- Electronics engineer (B.Sc. or higher);
- At least 4 years relevant experience in digital design and DSP/FPGA development in industrial environment, preferably in (aero)space business
- Hands-on knowledge and experience in VHDL modelling (for example based on bit-true MatLab models), synthesis, timing verification
- Understanding of modulation/demodulation techniques, coding techniques and other DSP techniques (specifically in the field of satellite communication equipment would be a strong benefit)
- Design and development experience of high-speed advanced digital circuitry, knowledge of signal integrity aspects
- Good team worker, pro-active, hands-on, engaged, takes full responsibility for his/her FPGA design
- Good verbal and written communication skills in international project teams

For a practical and motivated individual, this position will offer an exciting challenge where they can contribute directly in the growth of this quality-driven, dynamic SME organization.
If you are interested please contact us via email employment@celestia-sts.com

For further information related to the company, its products and activities refer to
www.celestia-sts.com